

I, Mitsunobu SUEHIRO

of 6-15, Kawaramachi 4-chome, Chuo-ku, OSAKA 541-0048 JAPAN

certify that I am conversant with the English language and that the attached is a true and accurate translation made by me of the Japanese original.

SIGNATURE Mitsunobu Suehino

DATE April 24th, 2007

[Document Name] Patent Application

[Serial Number] P3355

[Date of Application] July 17, 2002

[To:] Kozo Oikawa, Commissioner of Japanese Patent Office

[International Patent Classification] C30B 29/00

[Inventor]

[Address or Abode] 1-2-1, Shibaura, Minato-ku, TOKYO

[Name] Shinsuke SADAMITSU

[Inventor]

[Address or Abode] 1-2-1, Shibaura, Minato-ku, TOKYO

[Name] Nobumitsu TAKASE

[Inventor]

[Address or Abode] 1-2-1, Shibaura, Minato-ku, TOKYO

[Name] Hiroyuki TAKAO

[Inventor]

[Address or Abode] 1-2-1, Shibaura, Minato-ku, TOKYO

[Name] Kouji SUEOKA

[Inventor]

[Address or Abode] 1-2-1, Shibaura, Minato-ku, TOKYO

[Name] Masataka HORAI

[Applicant]

[Identification Number] 302006854

[Address or Abode] 1-2-1, Shibaura, Minato-ku, TOKYO

[Name] Sumitomo Mitsubishi Silicon Corporation

[Attorney]

[Identification Number] 100059373

[Address or Abode] Kawaramachi Naniwa Bldg., 4-6-15,
Kawara-machi, Chuo-ku, Osaka-shi, OSAKA

[Patent Attorney]

[Name] Motoshige SEIKEI

[Telephone] 06-6201-3851

[Attorney]

[Identification Number] 100088498

[Address or Abode] Kawaramachi Naniwa Bldg., 4-6-15,
Kawara-machi, Chuo-ku, Osaka-shi, OSAKA

[Patent Attorney]

[Name] Shoji YOSHIDA

[Telephone] 06-6201-3851

[Indication of Fee]

[Deposit Account No.] 008590

[Amount of Fee] 21,000 yen

[List of Submitted Items]

[Name of Item] Specification 1

[Name of Item] Drawing 1

[Name of Item] Abstract 1

[General Power of Attorney No.] 0203110

[General Power of Attorney No.] 0203108

[Necessity of Proof] Necessary

[Document Name] Specification

[Title of the Invention] High-resistance Silicon Wafer and Its
Manufacturing Method

[Scope of Claims for Patent]

[Claim 1] A high-resistance silicon wafer having
specific resistivity of $100\ \Omega\text{cm}$ or more, wherein a carbon
concentration is 5×10^{15} to 5×10^{17} atoms/cm³.

[Claim 2] The high-resistance silicon wafer according
to claim 1, wherein an oxygen concentration in the wafer is
beyond 8×10^{17} atoms/cm³ (Old-ASTM).

[Claim 3] The high-resistance silicon wafer according
to claim 1 or 2, wherein a DZ (Denuded Zone) layer is formed
at least $5\ \mu\text{m}$ or more in depth from a surface of the wafer.

[Claim 4] The high-resistance silicon wafer according
to any one of claims 1 to 3, wherein a density of a LPD (Light
Point Defect) having a size of $0.12\ \mu\text{m}$ or more and observed on
a surface of the wafer is controlled so as to be $0.2/\text{cm}^2$ or less.

[Claim 5] An epitaxial wafer having a high-resistance
silicon wafer according to any one of claims 1 to 4 as a base
wafer.

[Claim 6] An SOI wafer having a high-resistance silicon
wafer according to any one of claims 1 to 5 as a base wafer.

[Claim 7] The SOI wafer according to claim 6, which is
a bonded wafer or a SIMOX wafer.

[Claim 8] A method of manufacturing a high-resistance

silicon wafer, characterized in that a heat treatment which is effective in preventing an oxygen donor from being generated is performed on a silicon wafer having a specific resistivity of 100 Ωcm or more and a carbon concentration of 5×10^{15} to 5×10^{17} atoms/cm³.

[Claim 9] The method of manufacturing a high-resistance silicon wafer according to claim 8, wherein a remaining oxygen concentration after the heat treatment is more than 8×10^{17} atoms/cm³ (Old-ASTM).

[Claim 10] The method of manufacturing a high-resistance silicon wafer according to claim 8, wherein the heat treatment is a high-temperature heat treatment at 800°C or higher.

[Claim 11] The method of manufacturing a high-resistance silicon wafer according to claim 8, wherein the heat treatment is an oxygen out-diffusion treatment for forming a DZ (Denuated Zone) layer on a wafer surface.

[Claim 12] The method of manufacturing a high-resistance silicon wafer according to claim 11, characterized in that after the oxygen out-diffusion treatment, a heat treatment for forming an oxygen precipitate nucleus, or the heat treatment for forming the oxygen precipitate nucleus and a heat treatment for growing an oxygen precipitate are performed.

[Claim 13] The method of manufacturing a

high-resistance silicon wafer according to claim 8, wherein the heat treatment is high-temperature annealing treatment for eliminating a COP which is a void defect caused by a hole from a wafer surface.

[Claim 14] A method of manufacturing an SOI wafer, characterized by manufacturing an SIMOX type of SOI wafer comprising a high-resistance silicon wafer having specific resistivity of $100\ \Omega\text{cm}$ or more and a carbon concentration of 5×10^{15} to 5×10^{17} atoms/cm³ as a base wafer.

[Claim 15] The method of manufacturing an SOI wafer according to claim 15, wherein a high-temperature heat treatment for forming a BOX layer in a SIMOX type of SOI wafer manufacturing process serves also as a heat treatment which is effective in preventing generation of an oxygen donor.

[Claim 16] A method of manufacturing an SOI wafer, characterized by manufacturing a bonded type of SOI wafer comprising a high-resistance silicon wafer having specific resistivity of $100\ \Omega\text{cm}$ or more and a carbon concentration of 5×10^{15} to 5×10^{17} atoms/cm³ as a base wafer.

[Claim 17] The method of manufacturing the SOI wafer according to claim 16, wherein the high-temperature heat treatment performed in the bonded type of SOI wafer manufacturing process serves also as a heat treatment which is effective in preventing the generation of the oxygen donor.

[Detailed Description of the Invention]

[0001]

[Technical Field to Which the Invention Pertains]

The present invention relates to a high-resistance silicon wafer used in a support substrate and the like in a high-frequency communication device and its manufacturing method.

[0002]

[Prior Art]

Recently, a demand for high-resistance substrate is increased along with the widespread use of a high-frequency communication device used in a short-distance wireless LAN. Conventionally, a compound semiconductor substrate such as GaAs has been mainly used as a support substrate of a RF (Radio Frequency : high frequency) circuit which requires high resistance. However, the compound semiconductor substrate is very expensive.

[0003]

Meanwhile, a silicon CMOS requires a large amount of power, so that it has been considered that it is not suitable for the RF circuit. However, because of recent considerable miniaturization and development of designing, it can be applied to the RF circuit. Therefore, a high-resistance silicon wafer which is excellent in RF characteristics and excellent in economical efficiency such as a mirror-surface silicon wafer

and a SOI (Silicon On Insulator) wafer using high-resistance crystal grown by the Czochralski method (CZ method) has attracted a lot of attention instead of the substrate of the compound semiconductor such as GaAs.

[0004]

However, since a quartz crucible is used when a silicon single crystal is manufactured by the CZ method, oxygen is contained in the crystal in an oversaturated state. Since an oxygen donor such as a thermal donor (TD) and a new donor (ND) is formed from this oxygen in a heat treatment in the process of forming a circuit of the device, there is a big problem such that resistivity of the wafer unstably varies on the side of a device manufacturer.

[0005]

Fig. 1 is a graph showing a relation between the oxygen donor and the wafer resistivity. In a case of the normal low-resistance wafer to which a dopant is added, since the oxygen donor slightly affects the resistivity of the wafer, there is no problem in a real operation. However, in a case of the high-resistance wafer in which the dopant is limited, when it is an n type, the specific resistivity is considerably reduced as the oxygen donor is increased. When it is a p type, although the specific resistivity is considerably increased along with the increase of the oxygen donor at first, if the oxygen donor is kept increasing, the p type is converted to the

n type, so that the specific resistivity is considerably decreased.

[0006]

In order to solve the above problem such that the resistivity considerably varies along with the increase of the oxygen donor, there is taken measures to prevent the oxygen donor from being formed by using a low-oxygen silicon wafer which is manufactured using a special crucible in which oxygen is prevented from being fused by a MCZ method or an inner face SiC coating. However, the low-oxygen silicon wafer which needs to use the MCZ method or the special crucible is surely expensive as compared with the general-purpose silicon wafer having a relatively high oxygen concentration which is manufactured by the normal CZ method. In addition, the oxygen lowering has a technical limitation. That is, in general, it is considered that concentration of 6×10^{17} atoms/cm³ or less is difficult to be implemented and a degree of 8×10^{17} atoms/cm³ is a limit in a wafer of 300 mm. In addition, in the silicon wafer having a low oxygen concentration, there is a problem of slipping and the like because of the lowering of mechanical strength caused by reduction in oxygen concentration.

[0007]

In order to solve the above problem, International Publication WO 00/55397 pamphlet discloses a technique in which a silicon single crystal rod having resistivity of 100 Ω cm or

more and initial interstitial oxygen concentration of 10 to 25 ppma [JEIDA] (7.9 to 19.8×10^{17} atoms/cm³ [Old-ASTM]) is grown, and a heat treatment for oxygen precipitation is performed on a silicon wafer cut from the above rod so as to limit the remaining interstitial oxygen concentration in the wafer to 8 ppma [JEIDA] (6.4×10^{17} atoms/cm³ [Old-ASTM]) or less.

[0008]

According to this technique, the manufacturing cost of the initial wafer becomes low because the general-purpose silicon wafer having a high initial oxygen concentration is used. Although the general-purpose silicon wafer having the high initial oxygen concentration is used, since the oxygen precipitating heat treatment is performed on the silicon wafer, the remaining oxygen concentration is lowered. Therefore, an oxygen donor is effectively prevented from being generated in a heat treatment for forming a circuit which is to be performed on the side of a device manufacturer. In the process of lowering the oxygen concentration in the wafer, a large amount of oxygen precipitate (BMD) is generated. Therefore, a gettering ability of the wafer is improved.

[0009]

[Problems to be Solved by the Invention]

However, according to the technique disclosed in the International Publication WO 00/55397 pamphlet, it is necessary to generate the large amount of oxygen precipitate (BMD) using

a high-resistance primary substrate having a high-oxygen concentration, and to sufficiently lower the remaining oxygen concentration of a product silicon wafer because of generation of the large amount of oxygen precipitate (BMD). However, this causes the following problems.

[0010]

First, to lower the remaining oxygen concentration in the product silicon wafer causes the mechanical strength of the wafer to be lowered. This is clear from the fact that slip dislocation generated from a wafer supporting part in the heat treatment is fixed by oxygen and as a result, a slip length is lowered as the oxygen concentration is increased [M.Akatsuka et al., Jpn. J. Appl. Phys., 36 (1997) L1422]. Meanwhile, the oxygen precipitate (BMD) is a factor of affecting the strength. The influence of BMD to the strength is complicated. For example, when the heat and stress of one's own weight added to the wafer is not so large, the movement of the slip dislocation is prevented and the strength is improved (International Publication WO 00/55397), but when the heat and the stress of one's own weight is large, the BMD itself becomes a source of the slip dislocation, so that the strength is lowered and the wafer is probably warped (K. Sueoka et al., Jpn. J. Appl. Phys., 36 (1997) 7095). The heat and the stress of one's weight applied to the wafer in the real device process depend on a device structure or a thermal sequence, and it is expected to be

increased in some cases. Therefore, in view of maintaining the mechanical strength of the wafer, if the BMD required for gettering is provided, the considerable lowering of the remaining oxygen by the excessive BMD generation described in the International Publication WO 00/55397 is not preferable.

[0011]

The second problem is a heat treatment cost. That is, in order to generate a large amount of oxygen precipitate, the heat treatment for forming the oxygen precipitate nucleus and the heat treatment for growing the oxygen precipitate at a high temperature for a long time are needed. Therefore, the heat treatment cost is increased. As a result, although the manufacturing cost of the primary wafer is inexpensive, a price of a final product wafer becomes expensive.

[0012]

It is an object of the present invention to provide the high-resistance silicon wafer in which a gettering ability and economical efficiency is excellent, the oxygen donor is effectively prevented from being generated in the heat treatment for forming the circuit which is to be implemented on the side of the device manufacturer, and mechanical strength is high, and its manufacturing method.

[0013]

[Means for Solving the Problems]

In order to attain the above objects, the inventors of

the present invention have determined that a cause of the above problems lies in the excessive generation of the oxygen precipitate (BMD) and the considerable lowering of the remaining oxygen concentration in the product wafer caused by the excessive generation thereof, and studied about measures for preventing the generation of oxygen donor instead of the generation of the oxygen precipitate (BMD) in the high-resistance silicon wafer. As a result, they discovered that carbon doping is effective in preventing generation of the oxygen donor, especially in preventing generation of the thermal donor, and when a heat treatment is performed in addition to the carbon doping, the generation of the oxygen donor such as the thermal donor and a new donor is more effectively prevented, and many existing heat treatments which are generally used in a process of manufacturing various kinds of wafers such as a DZ wafer, a hydrogen annealed wafer, a SIMOX type and a bonded type of SOI wafer can be used as the heat treatment which is effective in preventing the generation of the oxygen donor.

[0014]

The fact that the carbon doping is effective in preventing the generation of the oxygen thermal donor is well known in a wafer having a normal resistance in which specific resistivity is less than $100\ \Omega$ (for example, refer to A. B. Bean and R. C. Newman J. Phys. Chem. Solids, 1972, Vol. 33, pp. 255-268).

However, in order to prevent the generation of the oxygen thermal donor in the wafer having the normal resistance, a carbon amount needs to be 1×10^{18} atoms/cm³. Since such carbon doping at high concentration accelerates dislocation in the growth of the single crystal by the CZ method and its crystallization becomes difficult, it is not realistic measures. However, it has become clear that in a high-resistance wafer having specific resistivity 100 Ω or more, an amount of carbon needed for preventing the generation of the oxygen thermal donor can be lowered to a realistic level such as 5×10^{15} to 5×10^{17} atoms/cm³ which does not hinder the single crystal crystallization.

[0015]

Thus, since the appropriate amount of carbon doping in the high-resistance wafer can effectively prevent the generation of the oxygen donor in the heat treatment for forming the circuits which is to be implemented on the side of the device manufacturer, it becomes unnecessary to excessively generate the oxygen precipitate (BMD), so that the heat treatment cost can be lowered. In addition, it is not necessary to considerably lower the remaining oxygen concentration in the product wafer, so that the mechanical strength of the wafer can be improved. Furthermore, as described in a document (M. Akatsuka and K. Sueoka, Jpn. J. Appl. Phys., 40 (2001) 1240), further improvement in mechanical strength can be implemented

by the appropriate amount of carbon doping. In addition, since generation of dislocation cluster when the single crystal is grown can be prevented by the carbon doping, non-defect crystal region in the growth of the single crystal can be expected to be enlarged.

[0016]

The reason why the carbon doping is effective in preventing the generation of the oxygen thermal donor is thought as follows. In addition, the reason why the doping amount of carbon can be reduced in the high-resistance wafer is thought as follows. Still further, the reason why the generation of the oxygen donor is prevented by the heat treatment of the carbon doped wafer is thought as follows.

[0017]

Although the reason why the carbon doping is effective in preventing the generation of the oxygen thermal donor is not completely figured out, according to A. B. Bean and R. C. Newman J. Phys. Chem. Solids, 1972, Vol. 33, pp. 255-268, for example, the reason is thought as follows. That is, the oxygen thermal donor is an O_n cluster in which 4 to 20 oxygen atoms are collected and it is generated at 400 to 500°C. When a carbon atom exists at an early stage of generation of such cluster, the carbon is captured by O_2 cluster which is a precursor to an electrically inactive thermal donor and C- O_2 cluster is generated. Therefore, subsequent generation of the

electrically active O_n ($n \geq 4$) can be prevented.

[0018]

In addition, in the silicon wafer having a normal resistance such as several Ωcm to several tens of Ωcm , it is necessary to generate the thermal donor of 10^{14} to 10^{16} atoms/ cm^3 in order to detect the generation of the oxygen thermal donor by variation of resistivity, and a high-concentration carbon such as 1×10^{18} atoms/ cm^3 is needed in order to prevent the generation of the thermal donor having such density. However, in the case of the high-resistance silicon wafer having resistance of 100 Ω or more, since resistivity is varied by the generation of the oxygen donor of 10^{14} atoms/ cm^3 or less, the carbon doping amount to prevent this generation can be reduced to 5×10^{15} to 5×10^{17} atoms/ cm^3 .

[0019]

In addition, it was found that when a heat treatment at 800°C or more was performed on the high-resistance silicon wafer having resistance of 100 Ω or more in which carbon was doped, the generation of the oxygen donor could be effectively prevented. Although the reason why the generation of the oxygen donor is prevented by the heat treatment at 800°C or higher and carbon doping is not clear at present, it is thought that the thermal donor which is a relatively small oxygen cluster and the new donor which is regarded as an initial form of the oxygen precipitate which is generated in the heat treatment at 600 to

700°C are grown, or decomposed and inactivated in the heat treatment at 800°C or higher.

[0020]

The high-resistance silicon wafer of the present invention is completed based on the above knowledge and it is a CZ silicon wafer having specific resistivity 100 Ω or more, in which a carbon concentration is 5×10^{15} to 5×10^{17} atoms/cm³.

[0021]

In addition, according to a manufacturing method of the high-resistance silicon wafer of the present invention, a heat treatment which is effective in preventing the generation of the oxygen donor, and preferably a high-temperature heat treatment at 800°C or higher is performed on the silicon wafer having the specific resistivity of 100 Ω or more and a carbon concentration of 5×10^{15} to 5×10^{18} atoms/cm³.

[0022]

Since the carbon concentration in the high-resistance silicon wafer according to the present invention is 5×10^{15} to 5×10^{17} atoms/cm³, the generation of the oxygen thermal donor can be prevented while the remaining oxygen concentration is maintained at high level, and the mechanical strength and the slipping resistance of the wafer can be improved by the high-concentration remaining oxygen and the carbon doping. In addition, excellent gettering ability can be provided by the generation of an appropriate amount of oxygen precipitate (BMD)

which is not influenced by the remaining oxygen concentration.

[0023]

When the carbon concentration in the wafer is less than 5×10^{15} atoms/cm³, the effect in preventing the generation of the oxygen donor is not sufficient. Meanwhile, when it is more than 5×10^{17} atoms/cm³, dislocation could occur while the crystal is grown and single crystal crystallization becomes difficult. An especially preferable carbon concentration is 1×10^{16} to 3×10^{17} atoms/cm³.

[0024]

The preferable remaining oxygen concentration in the wafer is more than 8×10^{17} atoms/cm³ (Old-ASTM). When this is 8×10^{17} atoms/cm³ or less, the mechanical strength is lowered. Although an upper limit of the remaining oxygen concentration is not especially defined, it is preferably 25×10^{17} atoms/cm³ or less in view of concern of defect generation on the substrate surface because of excessive oxygen precipitate depending on a device heat treatment condition which is to be implemented on the side of the user because the oxygen precipitate is increased as the oxygen concentration is increased, or a limit of an initial oxygen concentration as will be described below. According to an especially preferable remaining oxygen concentration, its lower limit is 10×10^{17} atoms/cm³ or more and its upper limit is 20×10^{17} atoms/cm³ or less and more preferably, 16×10^{17} atoms/cm³ or less.

[0025]

The high-resistance silicon wafer of the present invention can be any type. The type will be illustrated below.

[0026]

1. Although it is thought that an appropriate amount of oxygen precipitate is effective in providing the gettering ability in the high-resistance silicon wafer of the present invention, the oxygen precipitate could become a harmful defect in the device formation on the other hand and especially the oxygen precipitate existing on the surface layer of the wafer causes deterioration of the device characteristics. Therefore, it is desirable that the oxygen precipitate is eliminated from the surface layer of the wafer at least. In this respect, the present invention can be applied to a DZ wafer on which DZ (Denuded Zone) is formed in depth of at least 5 μm or more from a wafer surface.

[0027]

The DZ layer can be formed by OD (Oxygen Out-Diffusion) treatment. The OD treatment is preferably performed at 1100 to 1200°C for 1 to 4 hours. Since the OD treatment is performed at high temperature, it can be used also as the heat treatment which is effective in preventing the generation of the oxygen donor. In addition, by selecting an atmosphere in the OD treatment, a grown-in defect such as COP (Crystal Originated Particle : holes which are a void defect surrounded by (111)

surface) can be eliminated from the wafer surface layer.

[0028]

That is, as the OD treatment atmosphere, there are nitrogen gas, oxygen gas, hydrogen gas, argon gas and the like. The COP can be eliminated from the wafer surface layer in the atmosphere of the hydrogen gas, the argon gas and the mixture of these gases among the above OD treatment atmosphere. This is an effective processing for a wafer which is not free of the COP.

[0029]

For reference, the DZ layer is formed such that after a heat treatment at 1000°C for 16 hours in a dry oxygen atmosphere, a wafer is cleaved and a wafer cleaved face is etched away by 2 μm with a selected etching solution (HF : HNO₃ : CrO₃ : Cu (NO₃)₂ : H₂ O : CH₃ COOH = 1200 cc : 600 cc : 250 g : 40 g : 1700 cc : 1200 cc). Then, it is defined by a distance from the wafer surface to a first etch pit by an optical microscope.

[0030]

2. There is a high-speed temperature rising and falling RTA (Rapid Thermal Anneal) treatment called lamp anneal treatment also as a heat treatment which is similar to the OD treatment. The present invention is effective in the wafer which receives this treatment and the treatment can be used also as the heat treatment which is effective in preventing the generation of the oxygen donor.

[0031]

3. Apart from the above heat treatment, an IG (Intrinsic Gettering) treatment which aggressively forms the oxygen precipitate is performed in some cases in order to provide the gettering ability. Although the IG treatment is a heat treatment for forming the oxygen precipitate nucleus, a heat treatment for growing an oxygen precipitate is performed subsequently in some cases. The present invention is also effective in the wafer which receives this IG treatment, and above all, since the latter heat treatment for growing an oxygen precipitate is performed at high temperature, it can be used also as the heat treatment which is effective in preventing the generation of the oxygen donor.

[0032]

4. From the same viewpoint, the present invention is effective in an epitaxial wafer in which an epitaxial layer is formed on a wafer surface.

[0033]

5. From the same viewpoint, the present invention is effective in a base wafer of an SOI wafer. The SOI wafer may be either a bonded type or an SIMOX type. In manufacturing the bonded type of wafer, a high-temperature heat treatment is performed in a step of bonding the wafer. This high-temperature heat treatment can be used also as the heat treatment which is effective in preventing the generation of the oxygen donor.

Meanwhile, in manufacturing the SIMOX type, a high-temperature heat treatment is performed in order to form a BOX layer after oxygen ion injection. This high-temperature heat treatment can be used also as the heat treatment which is effective in preventing the generation of the oxygen donor.

[0034]

6. Although defect distribution of the wafer in the thickness direction of the wafer was focused on in the above, the wafer can be classified by defect distribution in the diameter direction of the wafer. The present invention can be effective in a normal wafer in which the COP exists at least in a part in the diameter direction as well as a wafer free of the COP, which is provided from a defect-free crystal in which the Grown-in defect such as a large-sized COP or dislocation cluster is eliminated from an entire region in the crystal diameter direction by an operation and the like in a crystal pulling process.

[0035]

7. Referring to the COP-free wafer, although it was described in the OD treatment, the present invention can be effective in not only the COP-free wafer provided from the defect-free crystal but also a COP-free annealed wafer in which the COP is eliminated from a surface layer by annealing at 1150 to 1200°C for 4 hours in the atmosphere of the hydrogen gas, the argon gas and the mixture of these gases. This annealing

can be used also as the heat treatment which is effective in preventing the generation of the oxygen donor. In addition, the COP-free wafer means a state in which a density of LPD (Light Point Defect) having a size 0.12 μm or more and observed on the wafer surface is controlled so as to be 0.2/cm² or less.

[0036]

According to the manufacturing method of the high-resistance silicon wafer of the present invention, since the carbon concentration in the wafer is 5×10^{15} to 5×10^{17} atoms/cm³, the oxygen donor, especially the thermal donor can be effectively prevented from being generated while the remaining oxygen concentration is maintained at high level. In addition, when the heat treatment at 800°C or higher is performed in addition to the carbon doping, not only the thermal donor but also the new donor can be effectively prevented from being generated, so that the specific resistivity can be stabilized by both of them.

[0037]

The initial oxygen concentration (oxygen concentration before the heat treatment) in the silicon wafer is selected from a range such that the remaining oxygen concentration becomes more than 8×10^{17} atoms/cm³ (Old-ASTM) in view of the generation amount of the BMD. Quantitatively, considering that there is a case where the BMD is not generated, it is preferably beyond 8×10^{17} atoms/cm³, and more preferably 10×10^{17} atoms/cm³ or

more. According to an upper limit of the initial oxygen concentration, in view of solid solution limit of oxygen and excessive oxygen precipitate as will be described below, it is preferably 25×10^{17} atoms/cm³ or less, and more preferably 20×10^{17} atoms/cm³ or less, and further preferably 18×10^{17} atoms/cm³ or less. When the oxygen concentration is extremely high, since the oxygen precipitate is excessively generated, a secondary defect such as a defect in the oxygen precipitate or in a laminated layer or dislocation is generated in a device active layer of the wafer surface layer, which deteriorates the device characteristics. However, since the oxygen precipitation treatment is not performed in some cases, the upper limit can be allowed to be until 25×10^{17} atoms/cm³.

[0038]

The remaining oxygen concentration after the heat treatment is preferably more than 8×10^{17} atoms/cm³ and 25×10^{17} atoms/cm³ or less (Old-ASTM) for the reason described above. In this case also, since some users do not care about the oxygen precipitate, the upper limit is allowed to be until 25×10^{17} atoms/cm³.

[0039]

The CZ silicon wafer receives various kinds of heat treatments which is peculiar to its type. These existing heat treatments such as the oxygen out-diffusion heat treatment, the RTA treatment, the SIMOX heat treatment, the bonding heat

treatment, the COP elimination annealing which are peculiar to the wafer type can be used also as the heat treatment which can be effective in preventing the generation of the oxygen donor as described above.

[0040]

Furthermore, in addition to the existing heat treatment which is peculiar to the wafer type, an exclusive treatment which is effective in preventing the generation of the oxygen donor is also effective. In addition, the heat treatment for forming the oxygen precipitate nucleus to generate the oxygen precipitate and also the heat treatment for growing the oxygen precipitate can be implemented exclusively or commonly.

[0041]

The oxygen out-diffusion heat treatment is preferably performed at 1100 to 1200°C for 1 to 4 hours. The oxygen is reduced in the wafer surface layer by the oxygen out-diffusion heat treatment and since the oxygen precipitate is prevented from being generated and grown, the DZ layer is formed. Furthermore, the oxygen donor is prevented from being generated.

[0042]

The high-temperature oxidation heat treatment for forming the BOX layer in manufacturing the SIMOX wafer is normally performed at 1250 to 1400°C for 1 to 20 hours. The heat treatment atmosphere in the treatment comprises oxygen gas,

argon gas or mixture of these.

[0043]

When the bonded wafer is manufactured, in addition to the heat treatment for the purpose of bonding, the oxidation treatment is performed at 1100°C or higher in order to form a thermally oxidized film having a desired film thickness on the surface of the support substrate. The both heat treatments are effective in preventing the generation of the oxygen donor.

[0044]

As the COP elimination annealing, treatment at 1150 to 1200°C for 4 hours in the atmosphere of the hydrogen gas, the argon gas or the mixture of these gases is preferable.

[0045]

In the IG treatment for providing the gettering ability, the heat treatment for generating the oxygen precipitate nucleus is preferably performed at relatively low temperature such as 550 to 850°C for 1 to 16 hours, and the heat treatment for growing the oxygen precipitate is preferably performed at high temperature such as 900 to 1100°C for 1 to 20 hours. The latter heat treatment for growing the oxygen precipitate is effective in preventing the generation of the oxygen donor because of its temperature.

[0046]

As described above, in the high-resistance silicon wafer according to the present invention, while the generation of the

oxygen thermal donor can be prevented by the carbon doping, the generation of the oxygen donor can be effectively prevented in many kinds of wafers by using various kinds of heat treatments which are peculiar to the wafer type without using any special operation. Therefore, the generation of the thermal donor and the new donor can be prevented only by carbon doping substantially. As a result, lowering of unstable specific resistivity because of the generation of the oxygen donor can be prevented with extremely economical efficiency.

[0047]

[Modes for Carrying out the Invention]

Hereinafter, first to fourth embodiments of the present invention will be described.

[0048]

(First Embodiment: DZ wafer)

As the first step, a high-oxygen, high-resistance and carbon-doped silicon single crystal is formed by a normal CZ method. As the second step, a primary substrate in which for example, an amount of oxygen is 10 to 18×10^{17} atoms/cm³, an amount of carbon is 5×10^{15} to 5×10^{17} atoms/cm³ and resistance is high ($100 \Omega\text{cm}$ or more) is formed from the silicon single crystal.

[0049]

As the third step, an OD treatment is performed on the primary substrate at 1100 to 1200°C for 1 to 4 hours. By this

OD treatment, an oxygen precipitate (BMD) is prevented from being generated in the heat treatment as will be described below. Thus, a DZ (Denuded Zone) layer is formed at least 5 μm or more in depth from a surface of a product wafer.

[0050]

The OD treatment can be performed in an atmosphere of a mixed gas of nitrogen gas and oxygen gas. In addition, it can be performed in a hydrogen gas atmosphere. Furthermore, it can be performed in argon gas atmosphere. Still in addition, it can be performed in an atmosphere of a mixed gas of hydrogen gas and argon gas.

[0051]

As the fourth step, a heat treatment for forming a nucleus of the oxygen precipitate is performed on the primary substrate at 550 to 850°C for 1 hour or more. It is performed at 700°C or more preferably. Then, as a heat treatment for growing the oxygen precipitate, a heat treatment is performed at 900 to 1100°C for 1 hour or more.

[0052]

By the heat treatment at the fourth step, the oxygen precipitate (BMD) is formed at a density of 1×10^4 pieces/ cm^2 or more in a silicon wafer having high resistance of 100 Ωcm or more. Thus, the oxygen concentration in the wafer is reduced, for example, to be more than 8×10^{17} atoms/ cm^3 and 16×10^{17} atoms/ cm^3 or less.

[0053]

The silicon wafer product as thus manufactured is shipped to a device manufacturer. The characteristics of the product are described below.

[0054]

First, since a general-purpose silicon wafer having a relatively high oxygen concentration is used as the primary substrate, it is economically efficient. Secondly, since the oxygen precipitate is eliminated from the wafer surface layer, it is superior in device characteristics. Thirdly, since the large-sized oxygen precipitate (BMD) is formed in the wafer at a high density, it is superior in gettering ability. Fourthly, since a final oxygen concentration is relatively high and carbon is doped, it is superior in mechanical strength and slipping resistance. Fifthly, because of an inhibitory action of oxygen thermal donor by the doped carbon and an inhibitory action of oxygen donor by various kinds of high-temperature heat treatment in the wafer manufacturing process, although the final oxygen concentration is at a relatively high level, a resistance value is prevented from being varied unstably because of generation of the oxygen donor in a heat treatment for forming a circuit which is to be performed on the side of the device manufacturer.

[0055]

In addition, when a hydrogen gas or an argon gas is

selected as the atmosphere in the OD treatment, a grown-in defect can be reduced or vanished and a density of a LPD (Light Point Defect) having a size of 0.12 μm or more observed on the wafer surface can be reduced to 0.2 pieces/ cm^2 or less.

[0056]

(Second Embodiment: Epitaxial wafer)

As the first step, a high-oxygen, high-resistance and carbon-doped silicon single crystal is formed by a normal CZ method. As the second step, a primary substrate in which for example, an amount of oxygen is 10 to 18×10^{17} atoms/ cm^3 or more, an amount of carbon is 5×10^{15} to 5×10^{17} atoms/ cm^3 and resistance is high (100 Ωcm or more) is formed from the silicon single crystal.

[0057]

As the third step, an OD treatment is performed on the primary substrate at 1100 to 1200°C for 1 to 4 hours. As the fourth step, a heat treatment for forming an oxygen precipitate nucleus is performed under a condition of at 550 to 850°C for 1 hour or more. As the fifth step, a heat treatment is performed at 900 to 1100°C for 1 hour or more as a heat treatment for forming a nucleus of the oxygen precipitate and as a heat treatment for growing the oxygen precipitate.

[0058]

As the sixth step, on each substrate processed until the second step, processed until the third step, processed until

the forth step, or processed until the fifth step, an epitaxial growth process of 5 μm is performed.

[0059]

An epitaxial silicon wafer product as thus manufactured has the following characteristics.

[0060]

In order to examine whether the oxygen donor such as thermal donor (TD) and new donor (ND) is prevented from being generated or not, a heat treatment for generating the thermal donor is performed at 400°C for 2 hours and a heat treatment for generating the new donor is performed at 750°C for 8 hours on the respective wafers. There is no oxygen donor formed in the wafer on which epitaxial layer is grown at any step.

[0061]

Especially, according to the wafer on which the epitaxial layer is grown after the second step, it is most superior in cost and it is effective as an epitaxial silicon wafer in which resistivity is not varied by generation of the oxygen donor even through the heat treatment of the device process.

[0062]

In addition, according to the wafer on which the epitaxial layer is grown after the third step, since oxygen on the surface of the substrate is dispersed outward, the BMD is not precipitated on the surface of the substrate during the epitaxial growing process or the heat treatment of the device

process, so that it is effective as a high-definition epitaxial silicon wafer in which the generation of a defect due to the BMD precipitation does not occur in the epitaxial layer.

[0063]

In addition, according to the wafer on which the epitaxial layer is grown in the fourth step, since the DZ layer is also formed and oxygen precipitate nucleus sufficiently exists in the wafer, when it is used in the device process including a heat treatment at high temperature for a long time, it can be expected that the oxygen precipitate is sufficiently grown by the heat treatment of the device process, so that it is effective as a epitaxial silicon wafer which is superior in a gettering ability.

[0064]

Furthermore, according to the wafer on which the epitaxial layer is grown in the fifth step, since the DZ layer is also formed and the BMD has been already sufficiently grown, it is effective as a epitaxial silicon wafer which provides enough gettering ability from an initial stage of the device process.

[0065]

Thus, according to each of the wafer on which the epitaxial layer is grown after each step, it is needless to say that variation in a resistance value because of generation of the oxygen donor in the heat treatment for forming the circuit which

is to be performed on the side of the device manufacturer can be avoided.

[0066]

(Third Embodiment: SIMOX wafer)

As the first step, a high-oxygen, high-resistance and carbon-doped silicon single crystal is formed by a normal CZ method. As the second step, a primary substrate in which for example, an amount of oxygen is 10 to 18×10^{17} atoms/cm³ or more, an amount of carbon is 5×10^{15} to 5×10^{17} atoms/cm³ and resistance is high (100 Ω cm or more) is formed from the silicon single crystal.

[0067]

As the third step, oxygen ions are accelerated to 30 to 200keV and ion injection is performed in a surface of the primary substrate at a density of about 10^{18} atoms/cm³. As the fourth step, a heat treatment is performed on the substrate provided at the third step at 1250 to 1400°C for 1 to 20 hours in an atmosphere of oxygen gas or argon gas or mixed oxygen and argon gas, to form a BOX layer (buried oxide film layer) in the substrate.

[0068]

The SIMOX wafer product as thus manufactured has following characteristics.

[0069]

Since a general-purpose silicon wafer having a relatively

high oxygen concentration is used as a primary substrate, it is superior in economical efficiency. Since a final oxygen concentration is relatively high and carbon is doped, it is superior in mechanical strength and slipping resistance. Because of an inhibitory action of oxygen thermal donor by the doped carbon and an inhibitory action of oxygen donor by various kinds of high-temperature heat treatments in the wafer manufacturing process, although the final oxygen concentration is at a relatively high level, a resistance value is prevented from being varied unstably because of generation of the oxygen donor in the heat treatment for forming the circuit which is to be performed on the side of the device manufacturer.

[0070]

(Fourth Embodiment: Bonded wafer)

As the first step, a high-oxygen, high-resistance and carbon-doped silicon single crystal is formed by a normal CZ method. As the second step, a primary substrate in which for example, an amount of oxygen is 10 to 18×10^{17} atoms/cm³ or more, an amount of carbon is 5×10^{15} to 5×10^{17} atoms/cm³ and resistance is high ($100 \Omega\text{cm}$ or more) is formed from the silicon single crystal.

[0071]

As the third step, although it depends on a manufacturing method of a bonded SOI wafer, a heat treatment is performed at about 1000°C in an atmosphere of oxygen in a series of wafer

manufacturing steps and a thermally oxidized film serving as a BOX oxide film is formed on the surface of the primary substrate.

[0072]

As the fourth step, the above primary substrate as a support substrate is bonded to another substrate serving as an active layer by heat treatment of about 800°C. In addition, in a case a thick SOI wafer is manufactured, as another substrate serving as the active layer, a high-resistance silicon substrate which is doped with carbon may be used similar to the support substrate.

[0073]

As the fifth step, the wafer on the side of the active layer is ground and etched to a thickness 0.5 μm .

[0074]

The bonded SOI wafer product as thus manufactured has the following characteristics.

[0075]

Since a general-purpose silicon wafer having a relatively high oxygen concentration is used as a primary substrate, it is superior in economical efficiency. Since a final oxygen concentration is relatively high and carbon is doped, it is superior in mechanical strength and slipping resistance. Because of an inhibitory action of oxygen thermal donor by the doped carbon and an inhibitory action of oxygen donor by various

kinds of high-temperature heat treatments, although the final oxygen concentration is at a relatively high level, a resistance value is prevented from being varied unstably because of generation of the oxygen donor in the heat treatment for forming the circuit which is to be performed on the side of the device manufacturer.

[0076]

[Working Examples]

Then, working examples of the present invention will be shown and compared with comparative examples to clarify the effect of the present invention.

[0077]

(First Working Example)

The following two kinds of 8-inch sample wafers were cut out from a silicon single crystal grown by the CZ method. The first sample wafer is a P type carbon non-doped product in which specific resistivity is 1000 Ωcm and an oxygen concentration is 5×10^{17} atoms/cm³. The second sample wafer is a P type carbon doped product in which specific resistivity is 1000 Ωcm , an oxygen concentration is 5×10^{17} atoms/cm³, and a carbon concentration is 1×10^{16} atoms/cm³.

[0078]

A heat treatment for eliminating the oxygen donor (DK treatment) was performed on both wafers at 650°C for 30 minutes and then the following three patterns of heat treatments were

performed. According to the first pattern, only an OD treatment was performed at 1150°C for 3.5 hours in a nitrogen atmosphere containing 3% of oxygen. According to the second pattern, after the OD treatment, an isothermal heat treatment was performed at 700°C for 1 to 8 hours or 750°C for 2 to 16 hours in the nitrogen atmosphere containing 3% of oxygen as a heat treatment for forming oxygen precipitate nucleus. According to the third pattern, after the isothermal heat treatment, a high-temperature heat treatment was further performed at 1000°C for 16 hours in the nitrogen atmosphere as a heat treatment for growing the oxygen precipitate.

[0079]

Then, a heat treatment for generating a thermal donor was performed on the carbon non-doped wafer and the carbon doped wafer on which the DK treatment was performed (subsequent heat treatment was not performed) at 400°C for 1 to 4 hours in the nitrogen atmosphere. After the heat treatment, an oxygen donor density was measured in such a manner that resistivity was measured by using a 4-probe method and an amount of oxygen donor was found from a difference between the resistivities after the DK treatment. The result is shown in Fig. 2. It is shown that the oxygen donor is prevented from being generated by the carbon doping only. Especially, until the treatment at 400°C for 2 hours, the oxygen donor is prevented from being generated on the carbon doped wafer as compared with the carbon non-doped

wafer.

[0080]

The result after the second pattern of heat treatment was performed on the carbon doped wafer is shown in Fig. 3. The isothermal heat treatment at 700°C for 1 to 8 hours or at 750°C for 2 to 16 hours is the heat treatment for forming the oxygen precipitate nucleus, and also the heat treatment for generating a new donor. When the OD treatment at 1150°C for 3.5 hours is performed, the oxygen donor is prevented from being generated after the isothermal heat treatment.

[0081]

After each heat treatment of the first, second and third patterns was performed on the carbon doped wafer, a heat treatment for generating the thermal donor was performed on each wafer at 400°C for 1 to 4 hours in the nitrogen atmosphere. The result from measurement of an oxygen donor density after the heat treatment for generating the thermal donor was shown in Fig. 4(a) to 4(c).

[0082]

As can be seen from comparison between Fig. 4(a) or 4(b) and Fig. 2, the oxygen donor is more prevented from being generated until the heat treatment at 400°C for 2 hours by the OD treatment at 1150°C for 3.5 hours or the subsequent heat treatment for generating the oxygen precipitate nucleus at 750°C. As shown in Fig. 4(c), the high-temperature heat treatment at

1000°C for 16 hours as the heat treatment for growing the oxygen precipitate is more effective in preventing the oxygen donor generation and its effectiveness is not influenced by the remaining oxygen concentration.

[0083]

For reference, the result when the third heat treatment was performed on the carbon non-doped wafer is shown in Fig. 5(a) and 5(b). As shown in Fig. 5(a), when the OD treatment is performed, the remaining oxygen concentration is not sufficiently lowered even if the treatment time at the isothermal heat treatment is increased, and it is difficult to prevent the oxygen thermal donor from being generated. In addition, as shown in Fig. 5(b), when the OD treatment is not performed, the remaining oxygen concentration is lowered (the oxygen precipitate is increased) as the treatment time in the isothermal heat treatment is increased and accordingly the oxygen donor is prevented from being generated, but its effect is not so much as the effect provided by the carbon doping shown in Fig. 4(c). In addition, since the DZ layer is not provided by the heat treatment without the OD treatment, it cannot be applied to the device.

[0084]

In order to provide the same effect as the effect by the carbon doping shown in Fig. 4(c) by generation of the oxygen precipitate in the heat treatment including the OD treatment,

the OD treatment was performed on the carbon non-doped wafer having an initial oxygen concentration of 15×10^{17} atoms/cm³ and the heat treatments at 700°C for 64 hours and at 1000°C for 16 hours were performed. However, the remaining oxygen concentration is only lowered to 12×10^{17} atoms/cm³ and the oxygen thermal donor is not sufficiently prevented from being generated, so that a further long time unrealistic heat treatment is needed.

[0085]

Incidentally, an allowable value of the oxygen donor here is the oxygen thermal donor of 1×10^{13} cm⁻³ or less, which is generated after the heat treatment at 400°C × 2 hours. In the device process, a sintering heat treatment is performed at 400°C or 350°C for about 30 minutes after a wiring process of metal such as Al or Cu. This metal wiring is laminated to be several layers and the above described heat treatment is performed on each layer. Therefore, even when the heat treatment at 400°C for about 2 hours is performed, it is necessary to maintain resistivity which is 100 Ωcm or more and preferably 1000 Ωcm or more. As the allowable value of the oxygen donor which satisfies these conditions, the above described value was set. Since there is a measurement error in the 4-probe measurement, in the case of the p type after the DK treatment, a value lower than the resistance value is provided in some cases. Its variation is within 5×10^{12} atoms/cm³ in terms of a donor amount.

[0086]

(Second Working Example)

The following 5 kinds of 8-inch sample wafers were cut out from a silicon single crystal grown by the CZ method. The first sample wafer is a P type carbon non-doped product in which specific resistivity is 1000 Ωcm and an oxygen concentration is 15×10^{17} atoms/cm³. The second to fifth sample wafers are P type carbon doped products in which specific resistivity is 1000 Ωcm , an oxygen concentration is 15×10^{17} atoms/cm³, and a carbon concentration is 5×10^{15} , 1×10^{16} , 5×10^{16} , 1×10^{17} atoms/cm³.

[0087]

A heat treatment for eliminating the oxygen donor (DK treatment) was performed on each of the above wafers at 650°C for 30 minutes and then the OD treatment was performed at 1150°C for 3.5 hours in a nitrogen atmosphere containing 3% of oxygen. Then, an isothermal heat treatment was performed in a nitrogen atmosphere containing 3% of oxygen as a heat treatment for forming oxygen precipitate nucleus at 700°C for 8 hours on the carbon non-doped wafer and at 750°C for 2 hours on the carbon doped wafer, and then, a high-temperature heat treatment was performed on these wafers in the nitrogen atmosphere as a heat treatment for growing the oxygen precipitate at 1000°C for 16 hours.

[0088]

A heat treatment for generating the oxygen thermal donor was performed at 400°C for 4 hours in the nitrogen atmosphere on each of the DZ-IG processed wafer. An amount of oxygen donors was calculated from the result from measurement of resistivity of each wafer after the heat treatment. In case of the carbon doped wafer, variation in resistivity is small and the amount of oxygen donors is within an allowable range, meanwhile in the case of the carbon non-doped wafer, the oxygen donors are generated beyond the allowable value. As the result, it is found that the oxygen donor is effectively prevented from being generated when the carbon concentration is 5×10^{15} atoms/cm³ or more.

[0089]

(Third Working Example)

The following 2 kinds of 8-inch sample wafers were cut out from a silicon single crystal grown by the CZ method. The first sample wafer is a P type carbon non-doped product in which specific resistivity is 1000 Ωcm and an oxygen concentration is 15×10^{17} atoms/cm³. The second sample wafer is a P type carbon doped product in which specific resistivity is 1000 Ωcm, an oxygen concentration is 15×10^{17} atoms/cm³, and carbon concentration is 1×10^{16} atoms/cm³.

[0090]

These wafers were annealed with hydrogen in a hydrogen atmosphere at 1200°C for 1 hour. An oxygen donor amount was

found by measuring resistivity after the heat treatment for generating the thermal donor was performed at 400°C for 4 hours in the nitrogen atmosphere just after the annealing. The result of oxygen donor amount thus obtained is shown in Fig. 6. The variation in resistivity is small and the amount of oxygen donor is not more than an allowable value in each wafer just after the annealing. Although the amount of oxygen donor generated in the carbon doped wafer is also not more than the allowable value after the heat treatment at 400°C for 4 hours, the amount was not less than the allowable value in the case of the carbon non-doped wafer. As the result, it is found that the existing heat treatment of hydrogen annealing on the carbon doped wafer can serve also as the heat treatment which is effective in preventing the generation of the oxygen donor.

[0091]

(Fourth Working Example)

A 8-inch wafer was cut out from a silicon single crystal grown by the CZ method. The wafer is a P type wafer in which specific resistivity is 2000 Ωcm , an oxygen concentration is 15×10^{17} atoms/cm³, and a carbon concentration is 1×10^{16} atoms/cm³.

[0092]

The heat treatment for eliminating the oxygen donor (DK treatment) was performed on the wafer at 650°C for 30 minutes and then the following heat treatment was performed. First,

the OD treatment was performed at 1150°C for 3.5 hours in the nitrogen atmosphere containing 3% of oxygen, and then the heat treatment for forming the oxygen precipitate nucleus was performed at 750°C for 2 hours in the similar atmosphere. Then, epitaxial growing treatment was performed on the wafer so as to provide a P type layer having specific resistivity 10 Ω cm and a thickness 5 μ m.

[0093]

Even when the heat treatment for generating the thermal donor was performed at 400°C for 2 hours and the heat treatment for generating the new donor was performed at 750°C for 8 hours on this epitaxial wafer, the specific resistivity of the substrate is maintained at 2000 Ω cm or more which is the same as that after the DK treatment.

[0094]

(Fifth Working Example)

The following 2 kinds of 8-inch sample wafers were cut out from a silicon single crystal grown by the CZ method. The first sample wafer is a P type carbon non-doped product in which specific resistivity is 1000 Ω cm and an oxygen concentration is 15×10^{17} atoms/cm³. The second sample wafer is a P type carbon doped product in which specific resistivity is 1000 Ω cm, an oxygen concentration is 15×10^{17} atoms/cm³, and a carbon concentration is 1×10^{16} atoms/cm³.

[0095]

After the heat treatment for eliminating the oxygen donor (DK treatment) was performed on both wafer at 650°C for 30 minutes, oxygen ions were sputtered into the surface at an acceleration voltage 100keV. Then, the substrate was put in a heat treatment furnace which is maintained at 700°C. Then, the furnace was heated up to 1320°C and kept for 10 hours. Then, the substrate was cooled down at 700°C and taken out of the furnace, whereby an SIMOX wafer was manufactured.

[0096]

The heat treatment for generating the thermal donor was performed at 400°C for 4 hours in the nitrogen atmosphere on the SIMOX wafers which was manufactured from the carbon doped wafer and the carbon non-doped wafer. The result from the measurement of the oxygen donor density after the heat treatment for generating the thermal donor is shown in Fig. 7. As shown in Fig. 7, the oxygen donor is prevented from being generated in the carbon-doped SIMOX wafer. Meanwhile, although the generation of the oxygen donor is prevented a little in the carbon non-doped SIMOX wafer, its effect is not so much as the effect in the carbon-doped SIMOX wafer.

[0097]

(Sixth Working Example)

The following 2 kinds of 8-inch sample wafers were cut out from a silicon single crystal grown by the CZ method. The first sample wafer is a P type carbon non-doped product in which

specific resistivity is 1000 Ωcm and an oxygen concentration is 15×10^{17} atoms/ cm^3 . The second sample wafer is a P type carbon doped product in which specific resistivity is 1000 Ωcm , an oxygen concentration is 15×10^{17} atoms/ cm^3 , and a carbon concentration is 1×10^{16} atoms/ cm^3 .

[0098]

After the heat treatment for eliminating the oxygen donor (DK treatment) was performed on each wafer at 650°C for 30 minutes, the heat treatment was performed on each wafer at 800°C in the oxygen atmosphere to form the thermally oxidized film on the surface of the primary substrate. Then, this substrate serving as a support substrate was bonded to another wafer serving as an active layer at 1150°C. Then, the wafer on the side of the active layer was ground and etched to a thickness 0.5 μm , whereby a bonded SOI wafer was manufactured.

[0099]

The heat treatment for generating the thermal donor was performed at 400°C for 4 hours in the nitrogen atmosphere on the bonded SOI wafers which were manufactured with the carbon doped wafer and the carbon non-doped wafer. An oxygen donor density was measured after the heat treatment for generating the thermal donor. Resistivity of the bonded carbon-doped wafer was small in variation from the resistivity after the DK treatment and generation of the oxygen donor was prevented within an allowable range. Meanwhile, according to the bonded

carbon non-doped wafer, although the generation of the oxygen donor was prevented a little, its effect was not so much as that in the bonded carbon doped wafer.

[0100]

[Effects of the Invention]

As described above, according to the high-resistance silicon wafer of the present invention, generation of the oxygen donor can be prevented in the heat treatment for forming circuits which is to be performed on the side of the device manufacturer by carbon doping without lowering a remaining oxygen concentration. Thus, the mechanical strength of the wafer can be maintained and heat treatment costs can be lowered. Furthermore, since an appropriate amount of oxygen precipitate is generated without being influenced by the remaining oxygen concentration, an excellent gettering ability can be provided.

[0101]

In addition, according to the manufacturing method of high-resistance silicon wafer of the present invention, the generation of the oxygen donor can be effectively prevented by performing the heat treatment which is effective in preventing the generation of the oxygen donor in addition to the carbon doping. Furthermore, since various kinds of heat treatments which are peculiar to a wafer type can be used as the above heat treatment, an increase in heat treatment costs can be avoided and excellent economical efficiency is provided.

[Brief Description of the Drawings]

[Fig. 1]

A graph showing an influence of an oxygen donor on resistivity of a wafer.

[Fig. 2]

A graph showing an influence of carbon doping on generation of the oxygen donor.

[Fig. 3]

A graph showing an influence of a heat treatment on generation of the oxygen donor in a carbon doped product.

[Fig. 4]

Figs. 4(a) to 4(c) are graphs each showing an influence of the kind of heat treatments on the generation of the oxygen donor in a carbon doped product.

[Fig. 5]

Figs. 5(a) and 5(b) are graphs each showing an influence of a remaining oxygen concentration on the generation of the oxygen donor in a carbon non-doped product.

[Fig.6]

A graph showing an influence of hydro annealing on the generation of the oxygen donor in the carbon doped wafer and the carbon non-doped wafer.

[Fig.7]

A graph showing an influence of a high-temperature heat treatment for forming a BOX layer in a manufacturing process

of a SIMOX wafer on the generation of the oxygen donor in the carbon doped wafer and the carbon non-doped wafer.



FIG 1

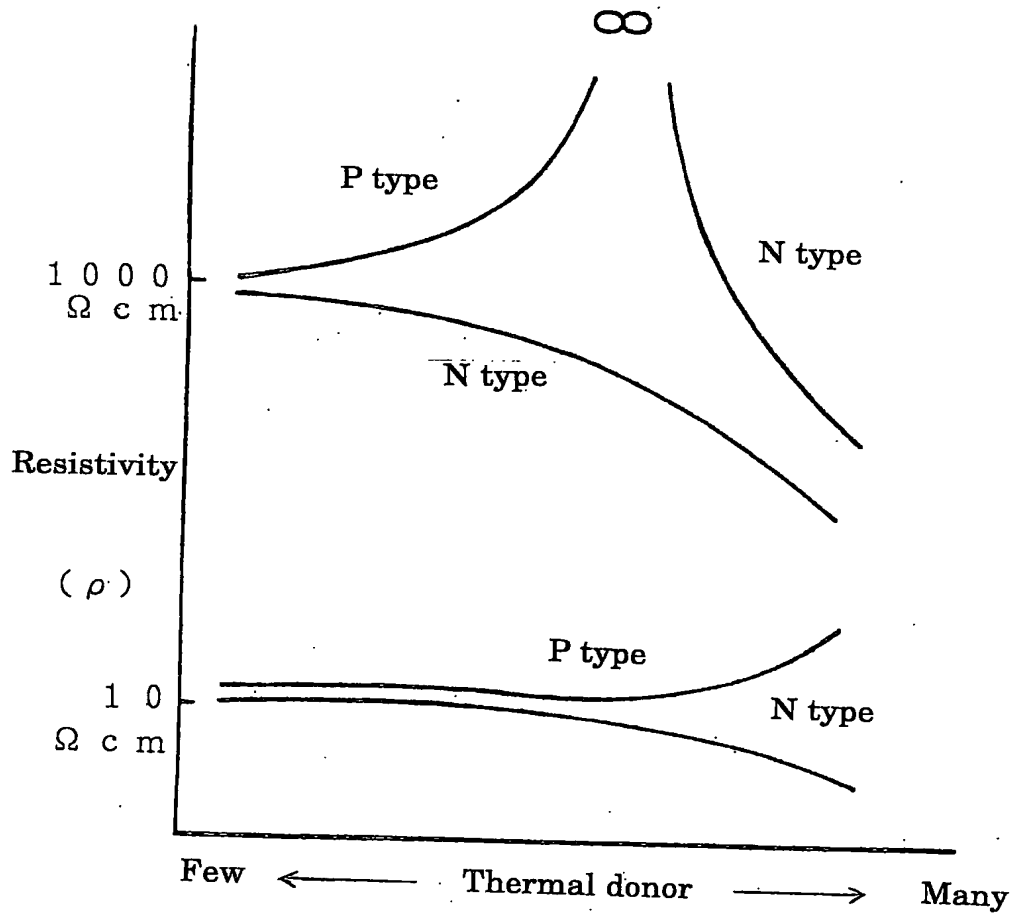


FIG 2

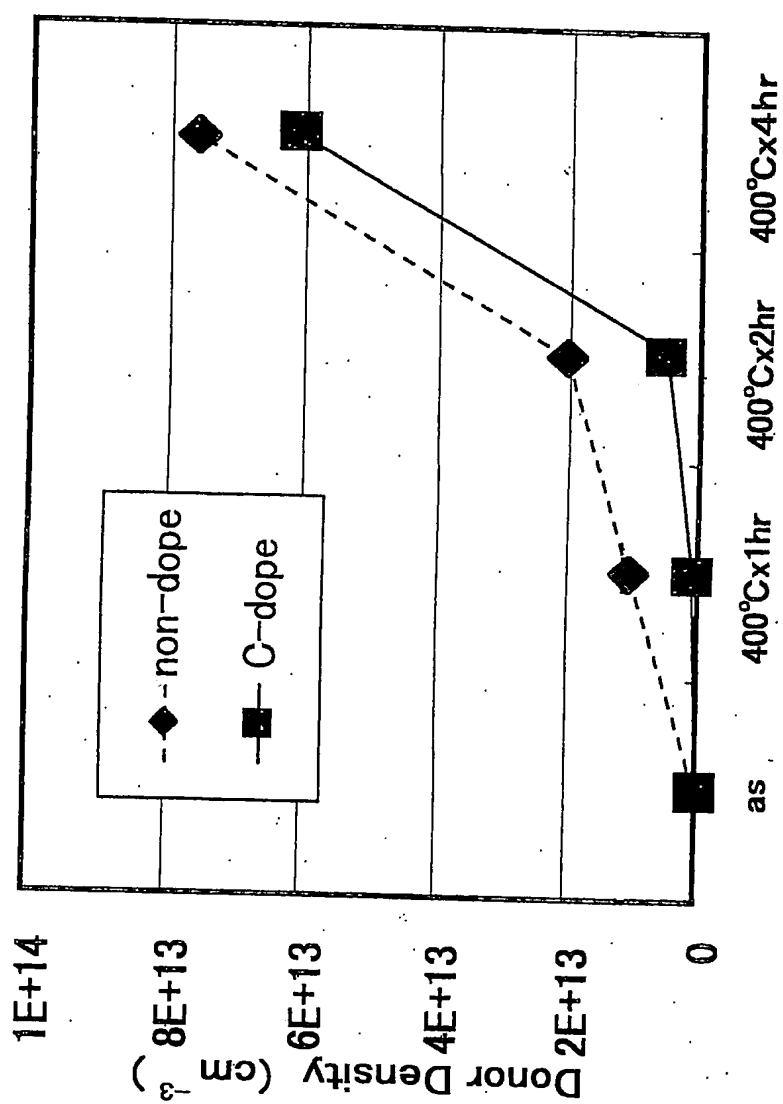


FIG 3

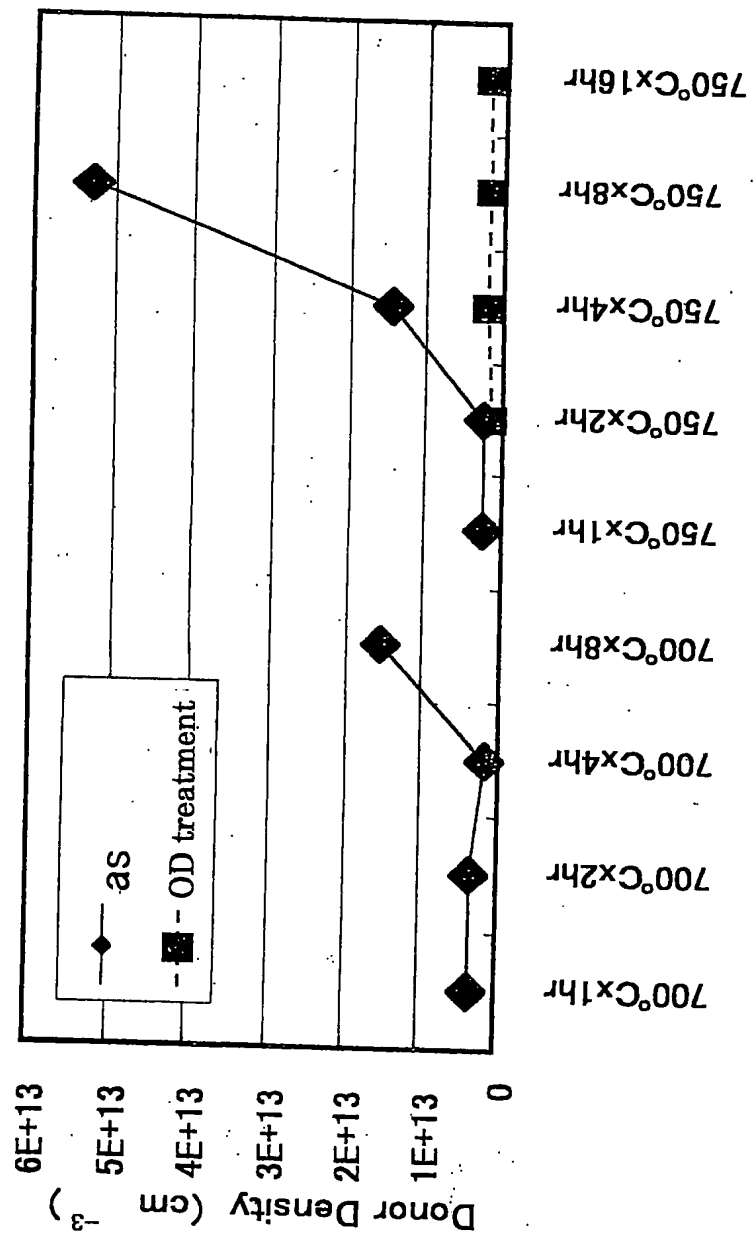


FIG 4

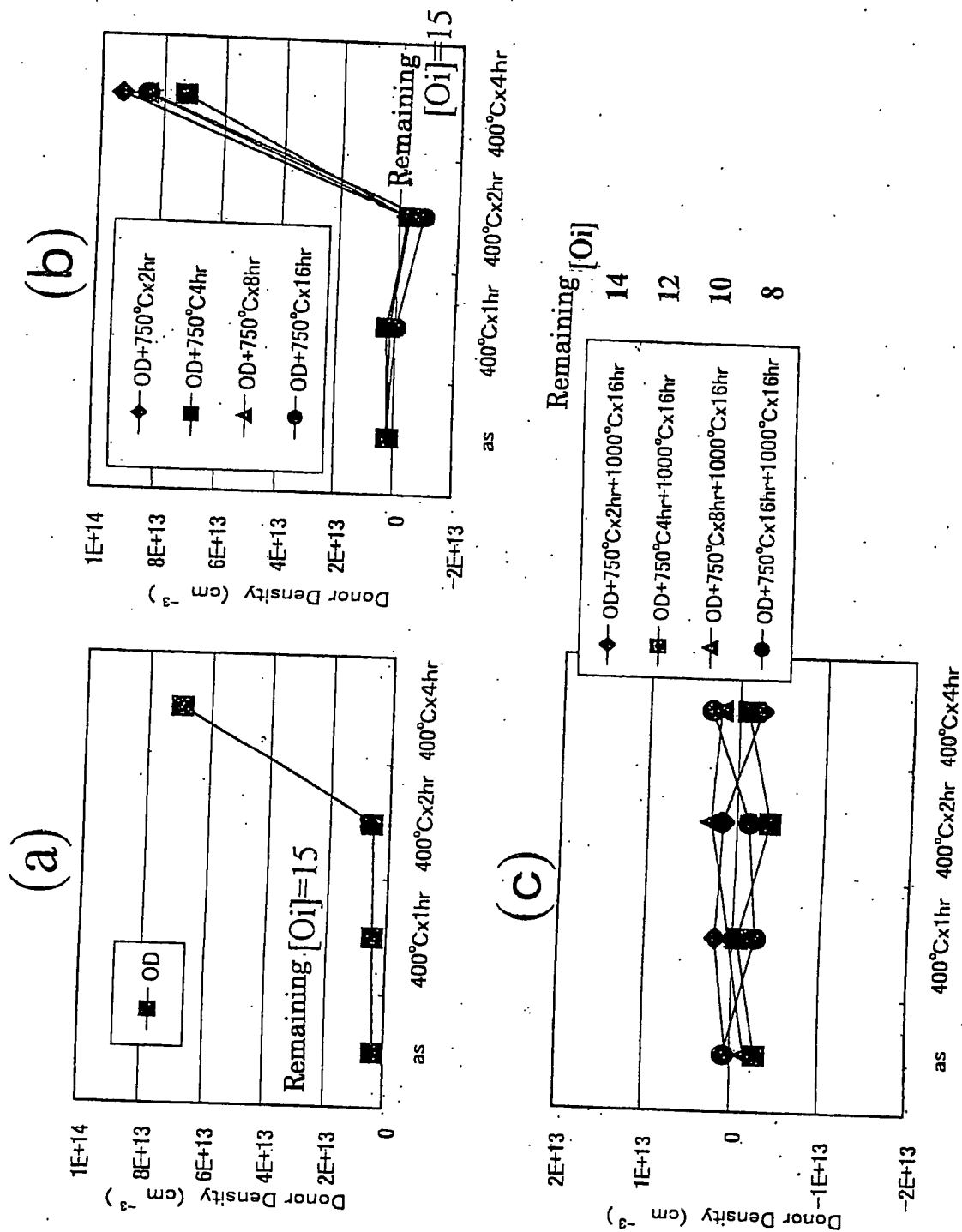


FIG 5

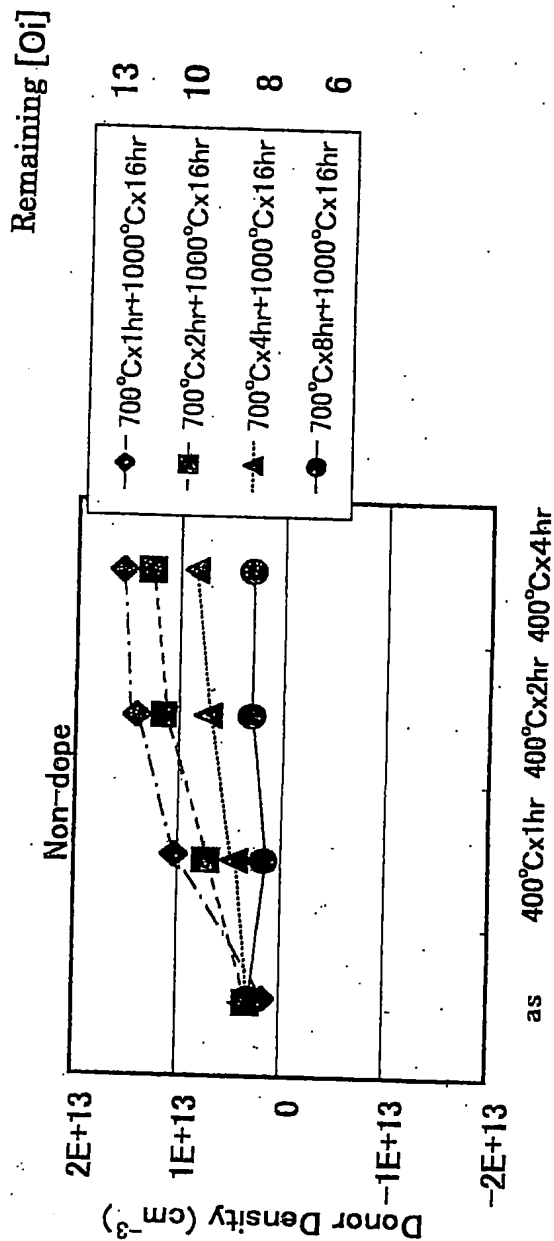
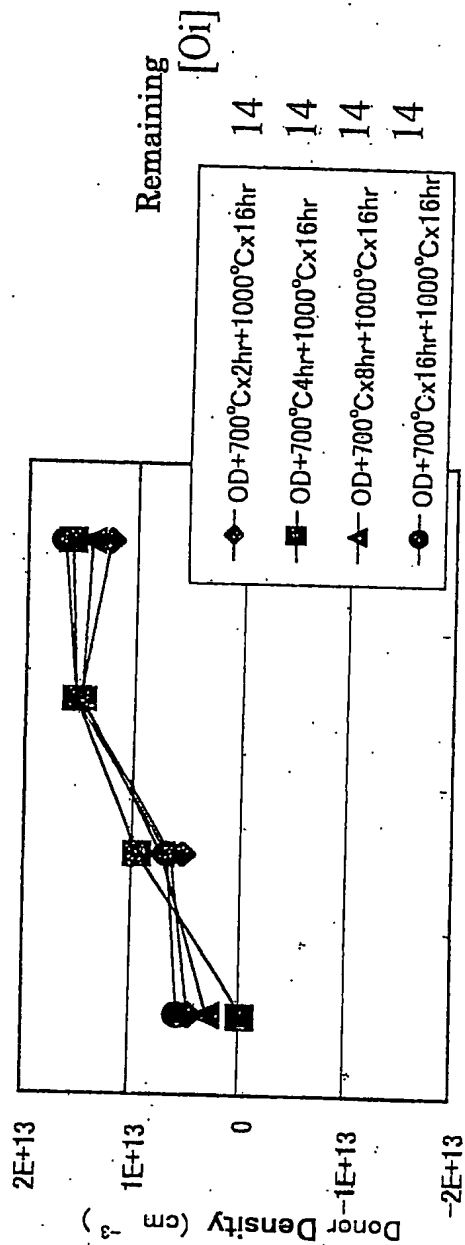


FIG 6

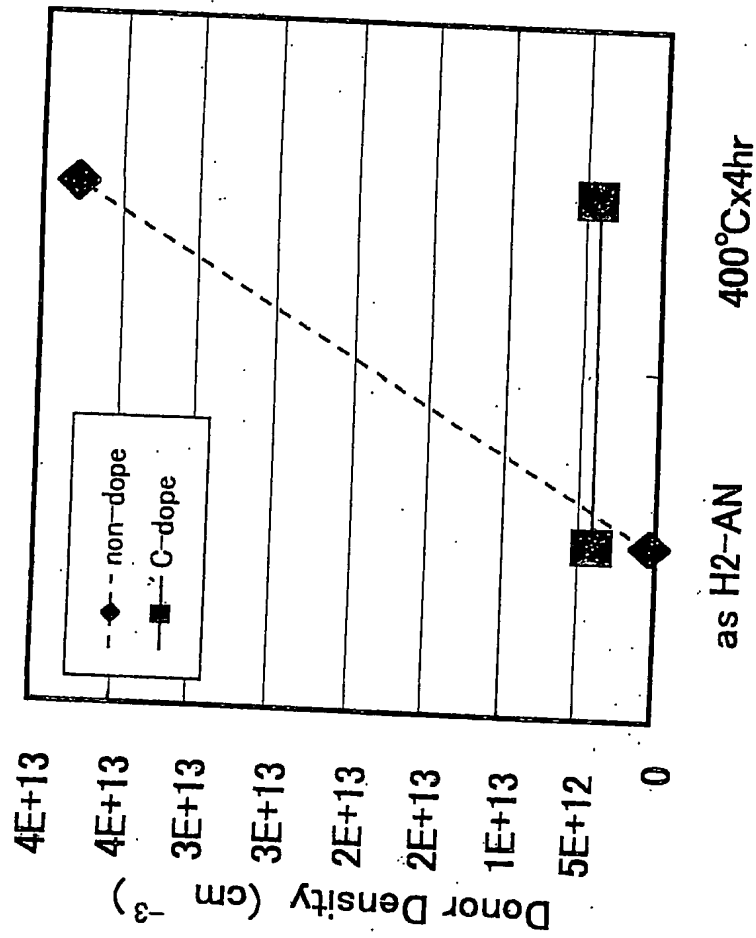
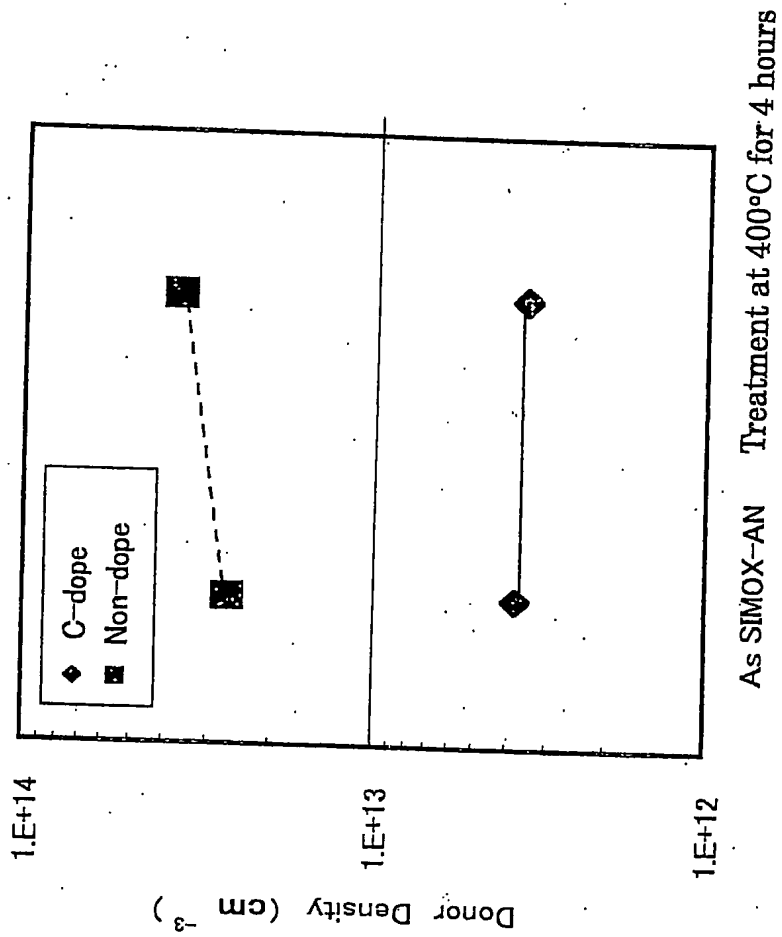


FIG 7



[Document Name] Abstract

[Abstract]

[Problem] A high-resistance silicon wafer is manufactured, in which a gettering ability and economical efficiency is excellent and an oxygen thermal donor is effectively prevented from being generated in a heat treatment for forming a circuit, which is to be implemented on the side of a device manufacturer.

[Solving Means] A high-temperature heat treatment at 800°C or higher is performed on a carbon doped high-resistance and high-oxygen silicon wafer in which specific resistivity is 100 Ωcm or more and a carbon concentration is 5×10^{15} to 5×10^{17} atoms/cm³ so that a remaining oxygen concentration becomes more than 8×10^{17} atoms/cm³ (Old-ASTM). As this high-temperature treatment, an OD treatment for forming a DZ layer on a wafer surface, a heat treatment for growing an oxygen precipitate for ensuring a gettering ability, a high-temperature annealing treatment for eliminating a COP on the surface layer, a high-temperature heat treatment for forming a BOX layer in a SIMOX wafer manufacturing process and the like can be used.

[Selected Drawing] None

Regarding Submission of IDS

Japanese Unexamined Patent Application Nos. 2002-127509 and 2002-360731 that are Japanese priority applications of the cited reference Takase (US10/512405) are not published before the filing date of the PCT application of the present invention (June 30, 2003). Thus, we consider submission of IDS is not necessary.

Details are as follows.

- Japanese Unexamined Patent Application No. 2002-127509 is not published in Japan, because this is considered withdrawn as a basic application of the application claiming domestic priority (Japanese Unexamined Patent Application No. 2002-360731).

- The publish date of Japanese Unexamined Patent Application No. 2002-360731 in Japan is January 8, 2004, which is after the filing date of the PCT application of the present invention (June 30, 2003).

- The publish date of the PCT application claiming priority of Japanese Unexamined Patent Application Nos. 2002-127509 and 2002-360731 is November 6, 2003, which is after the filing date of the PCT application of the present invention (June 30, 2003).